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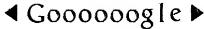
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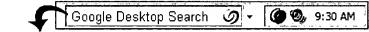
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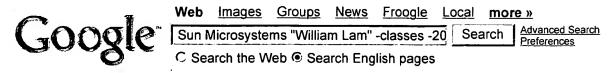
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Application #: 09990935 Filing Dt: 11/15/2001 Patent #: NONE **Issue Dt:**

PCT #: NONE Publication #: <u>US20030036894</u> Pub Dt: 02/20/2003

Inventor: William Lam

Title: Method and apparatus for amortizing critical path computations

Assignment: 1

Reel/Frame: 012321/0527 Received: Recorded: Mailed: Pages: 11/15/2001 01/22/2002

Conveyance: ASSIGNMENT OF ASSIGNORS INTEREST (SEE DOCUMENT FOR DETAILS).

Exec Dt: 11/13/2001

Assignee: SUN MICROSYSTEMS, INC.

901 SAN ANTONIO ROAD

PALO ALTO, CALIFORNIA 94303

Correspondent: COUDERT BROTHERS LLP

Assignor: LAM, WILLIAM

J.D. HARRIMAN II, ESQ.

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